

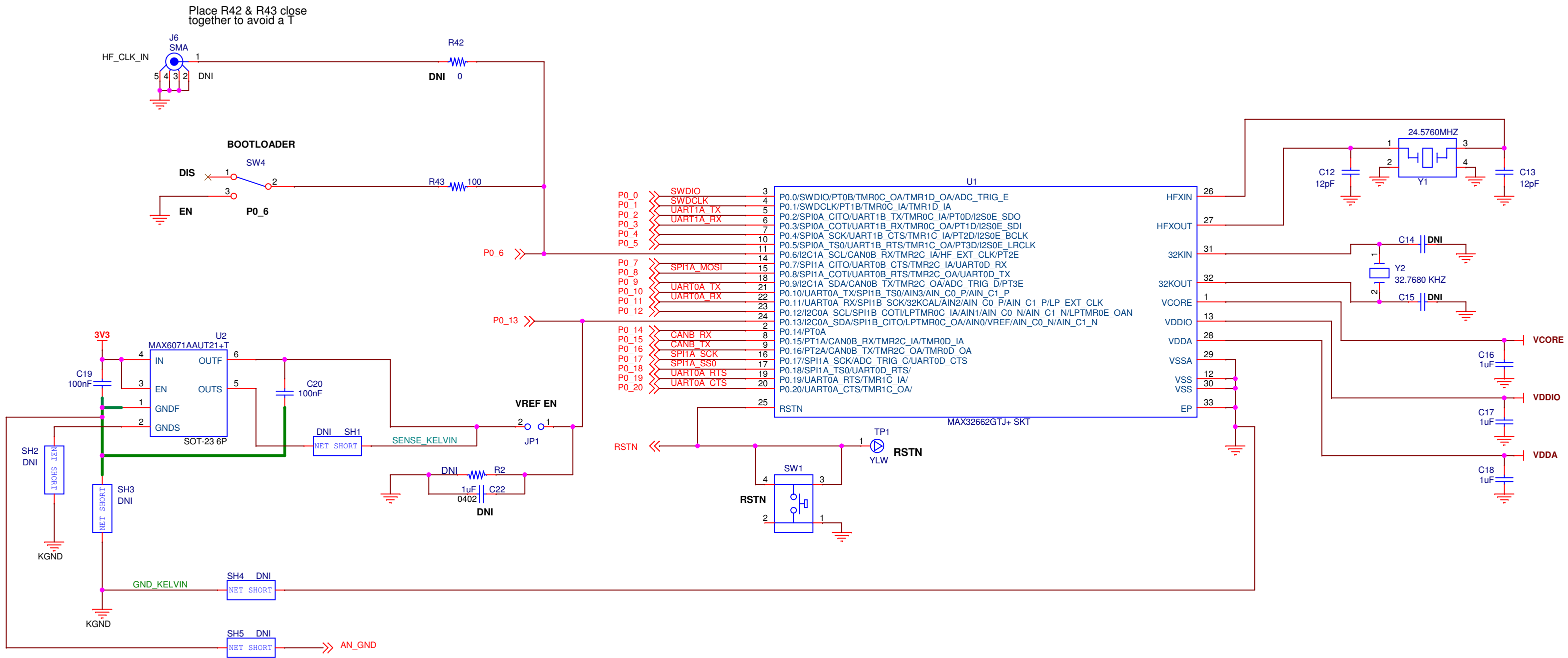
NOTES:

REV B

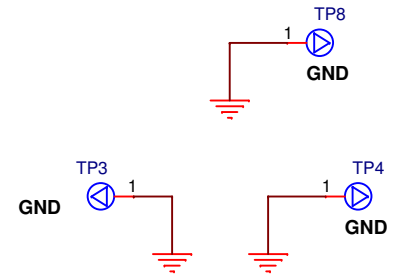
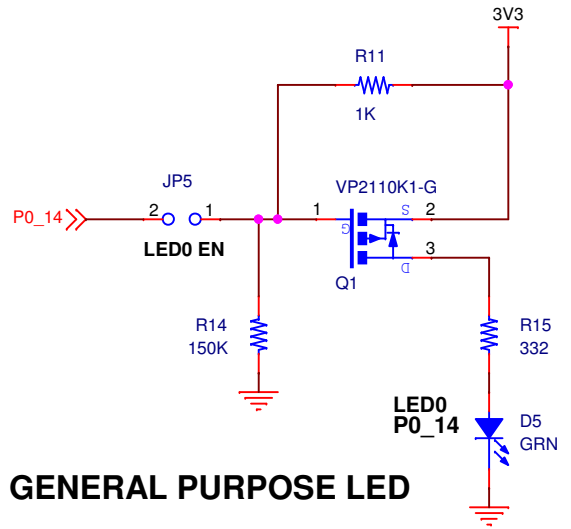
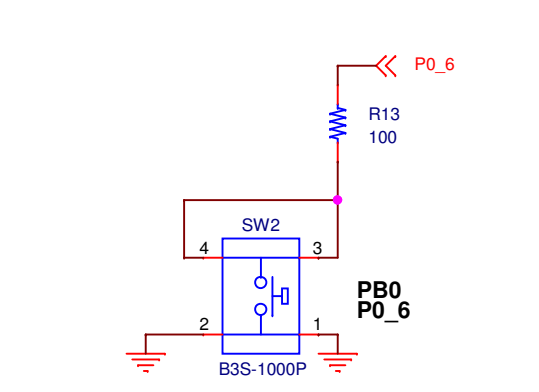
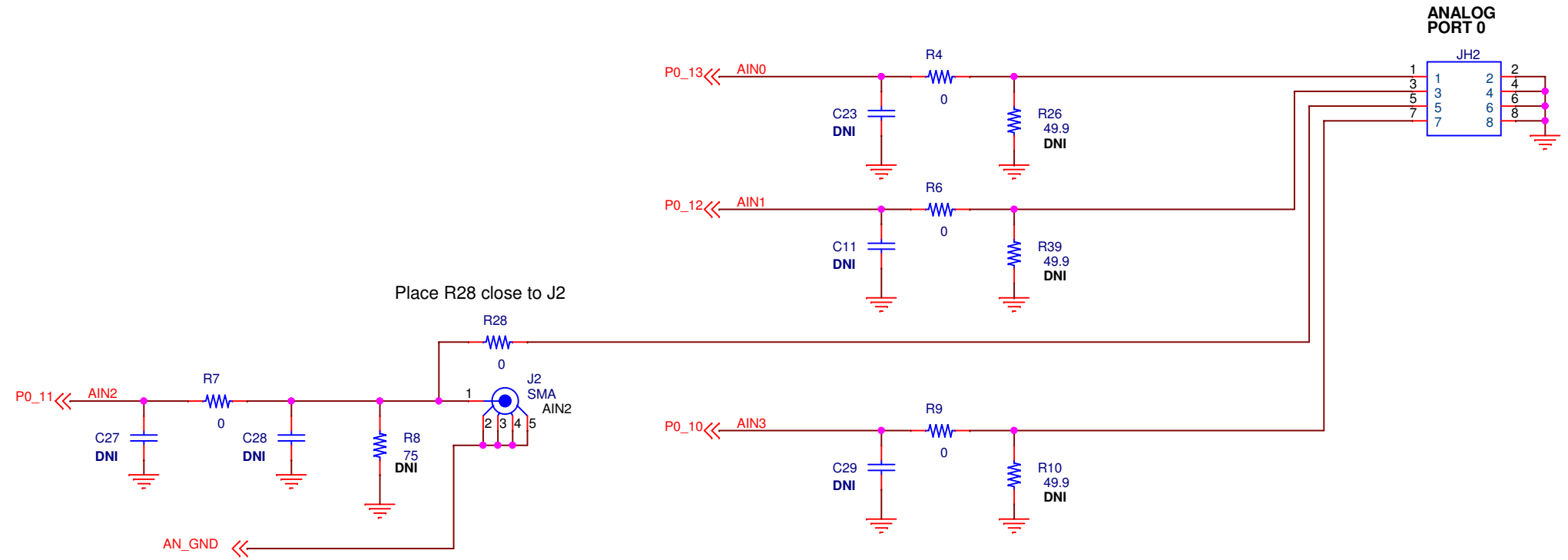
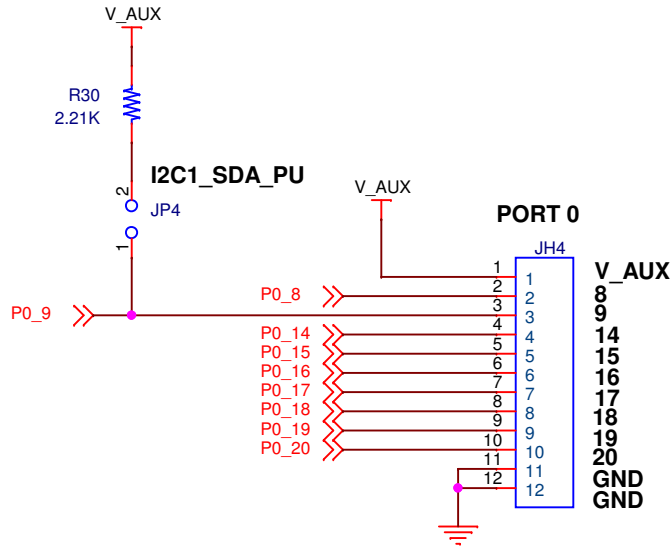
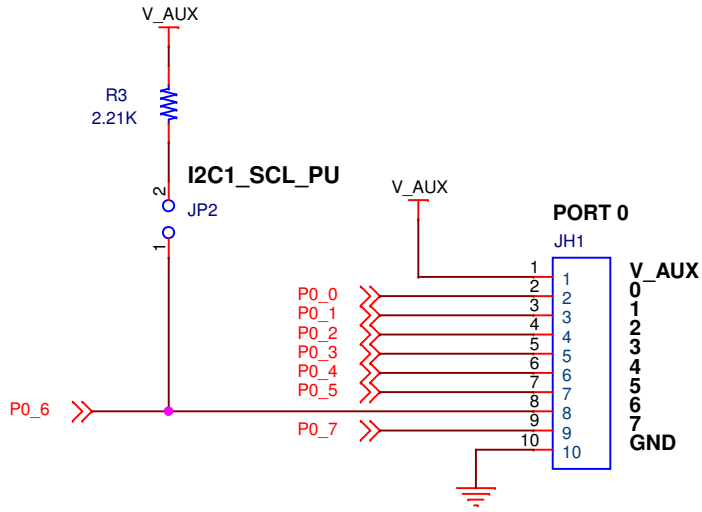
- 1) Disconnected VCC of the FTDI231x USB to serial bridge (U4) from VBUS of CN1 and connected to power switch SW3 pin 1 so that the bridge and board power up at the same time.
- 2) Corrected miswire on 24.576MHz crystal (Y1)
- 3) Changed C22 to a DNI on P0\_13
- 4) Connected UART1A to SWD connector for access by PICO2 debugger
- 5) Updated the MAX32662.DSN symbol to match the IC datasheet.

CONTENTS

- SHEET 1: REVISION HISTORY & NOTES
- SHEET 2: MAX32662, VREF & BOOTLOADER
- SHEET 3: GPIO, ANALOG, PB SWITCH & LED
- SHEET 4: USB to SERIAL PORT & SWD
- SHEET 5: CAN BUS & TFT DISPLAY
- SHEET 6: POWER & MECHANICALS



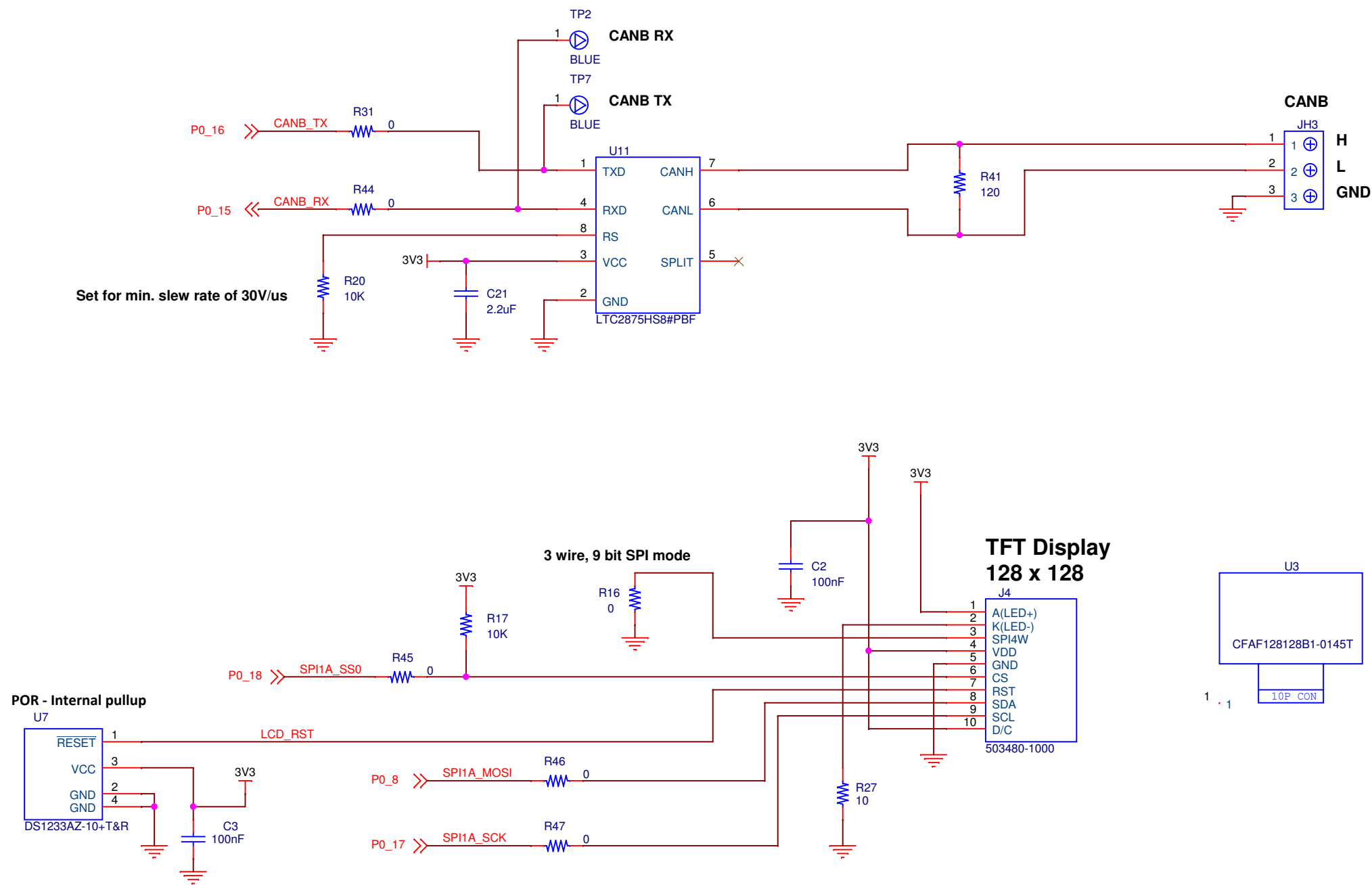
GPIO HEADERS

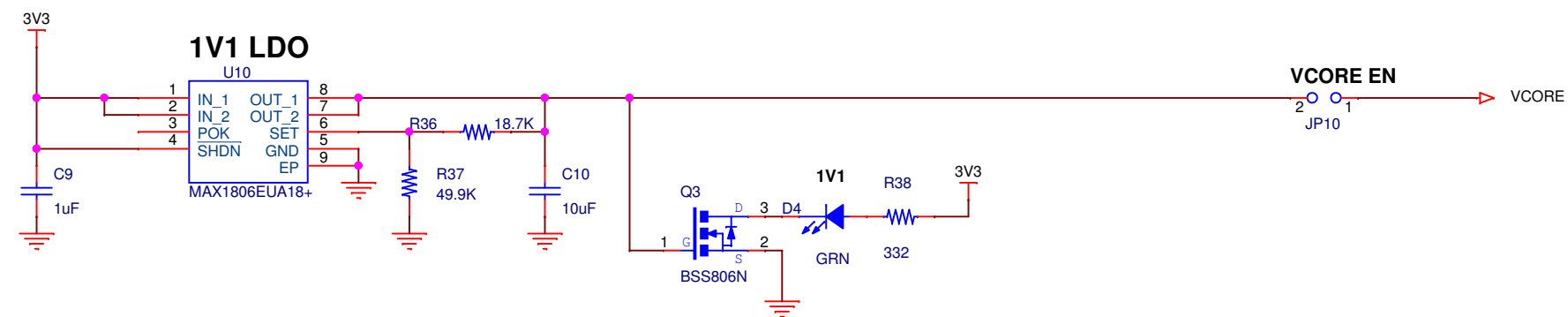
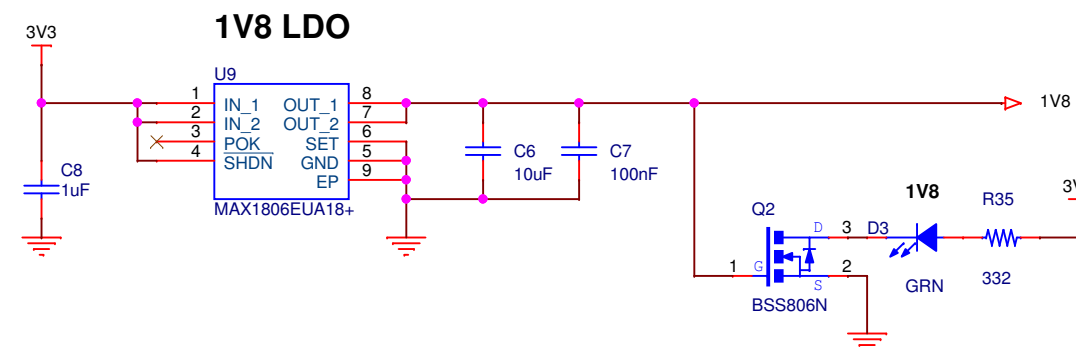
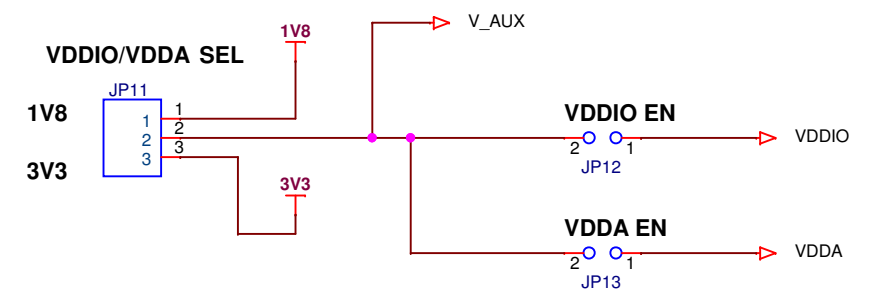
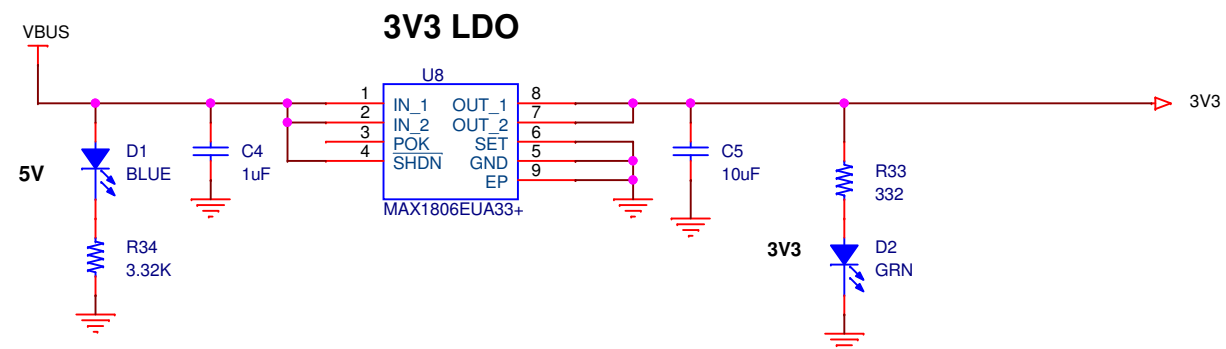


GENERAL PURPOSE PB SWITCH

GENERAL PURPOSE LED







## MECHANICALS

## PCB1

PCB-00237-B-0

### DAUGHTERBOARD STANDOFF HOLES



H2  
⊕  
DNI



H4  
DNI